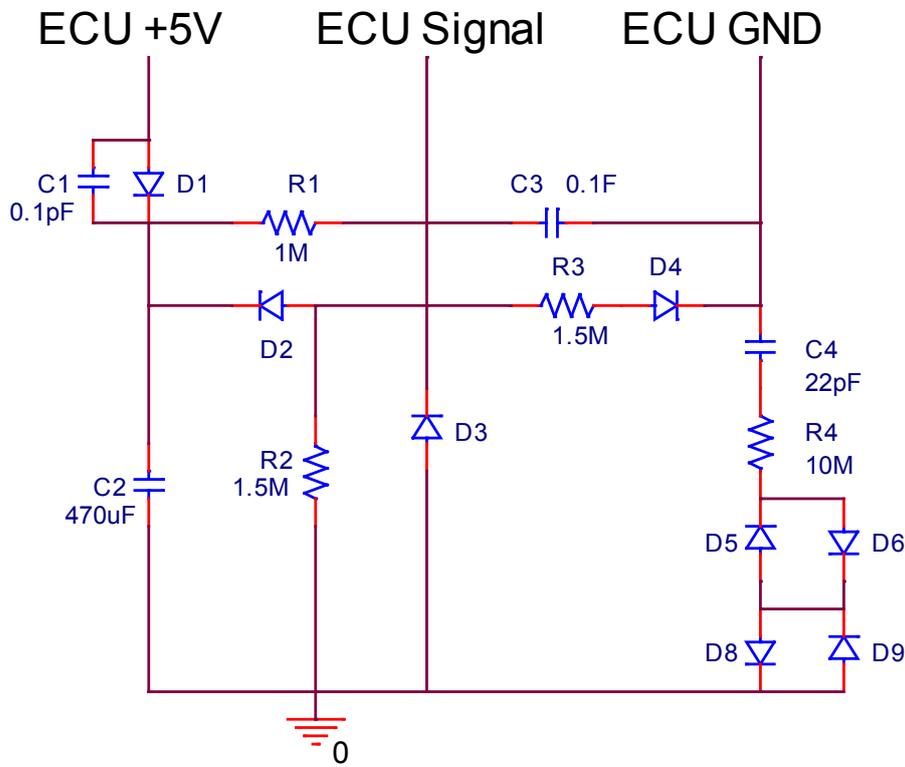
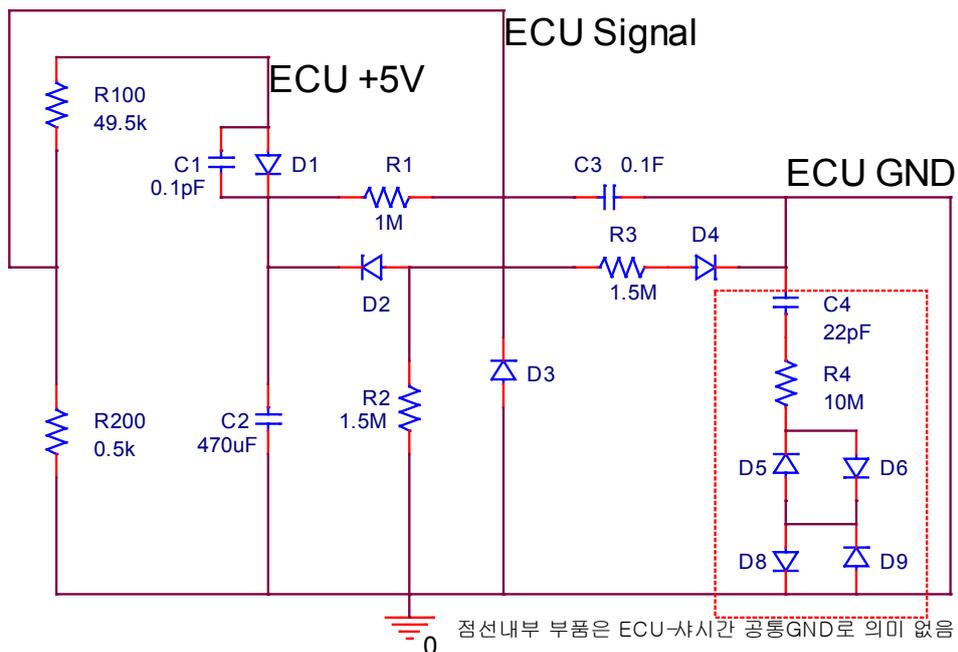


1. 회로도(특허등록상 원도)

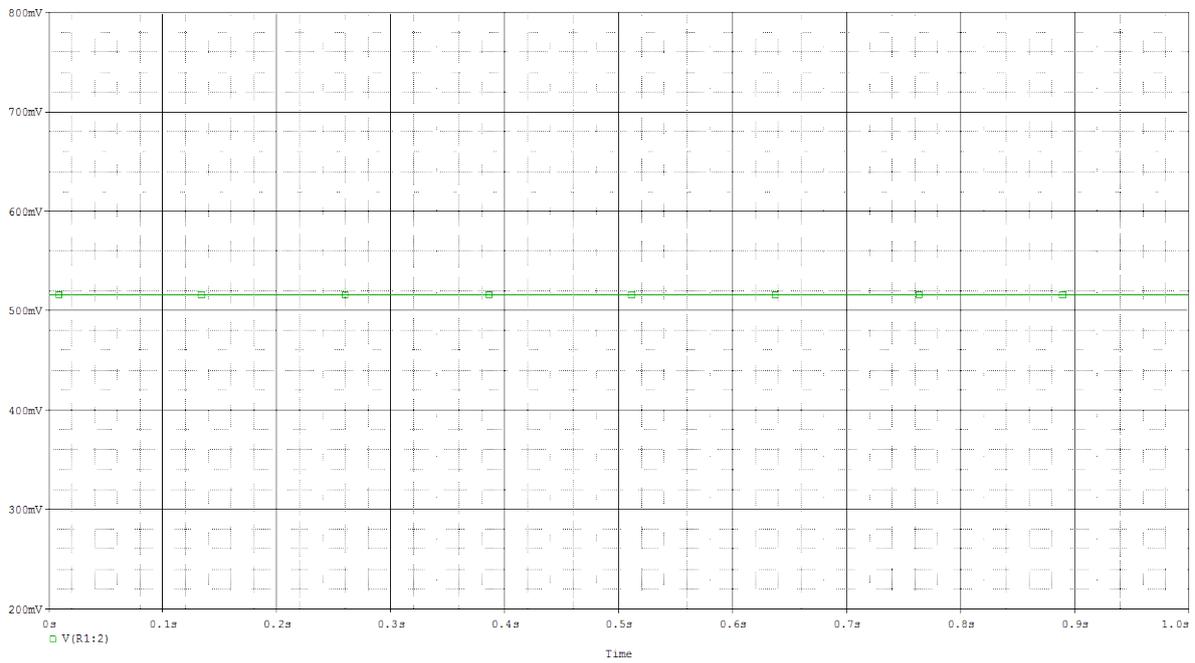


특허상에 등록된 원도를 Orcad로 옮기면 위와 같이 구성됨

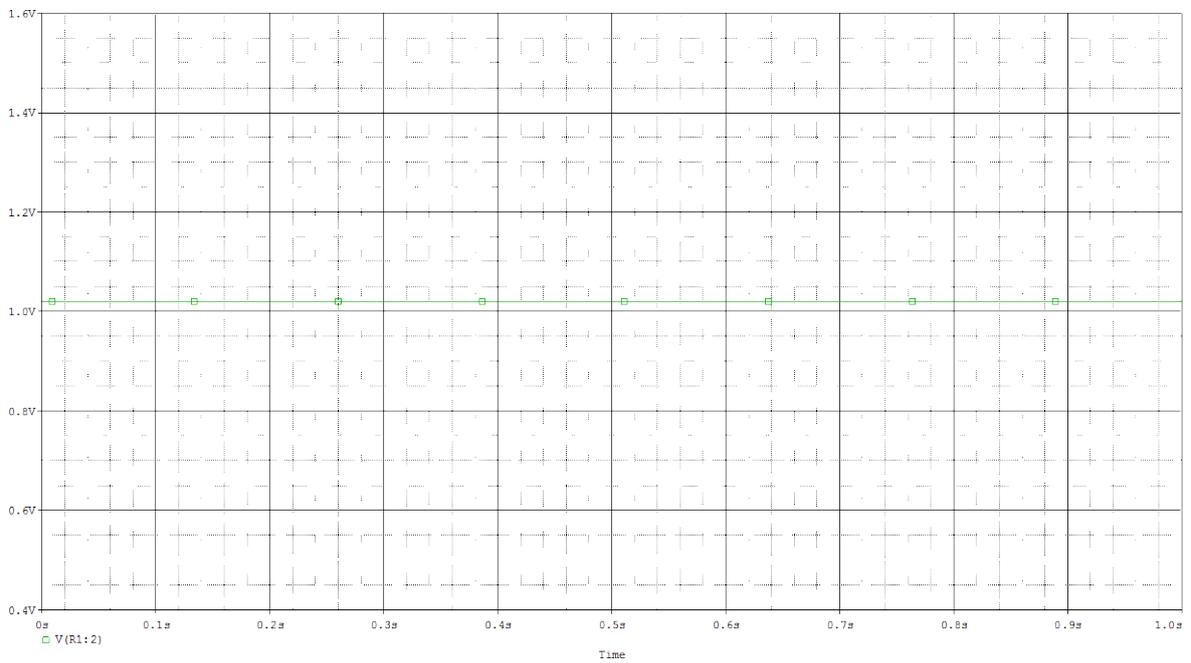
2. ECU(Idling 일때)와 VAD 구성 등가회로



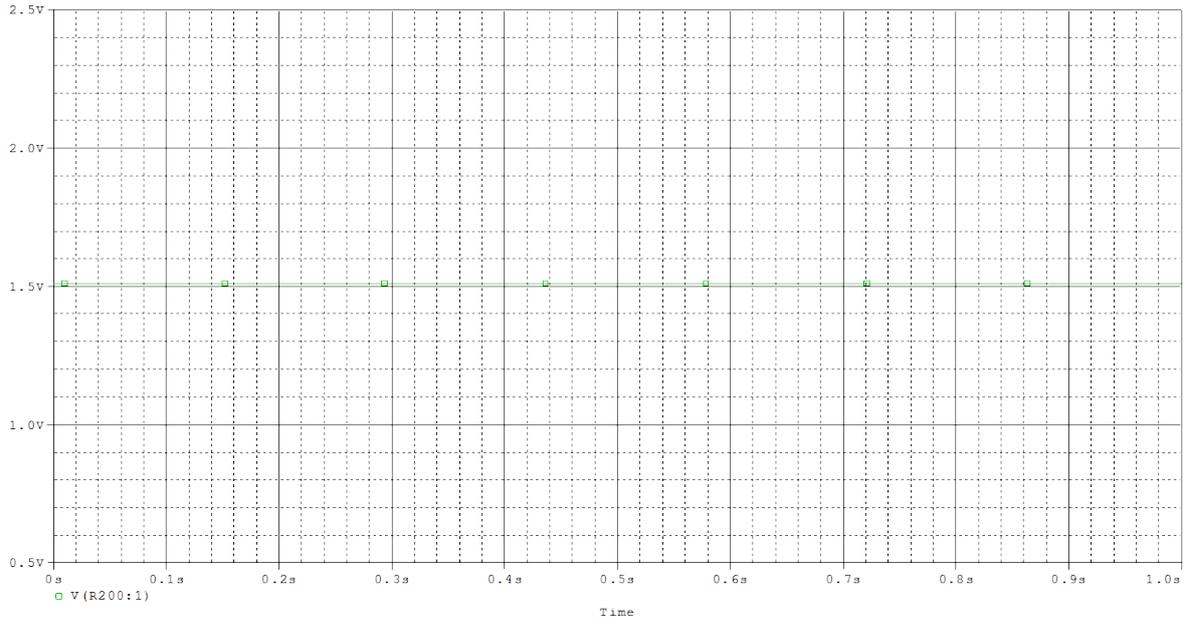
3. VAD장착후 등가회로 ECU Signal의 출력 전압(초록색 선)



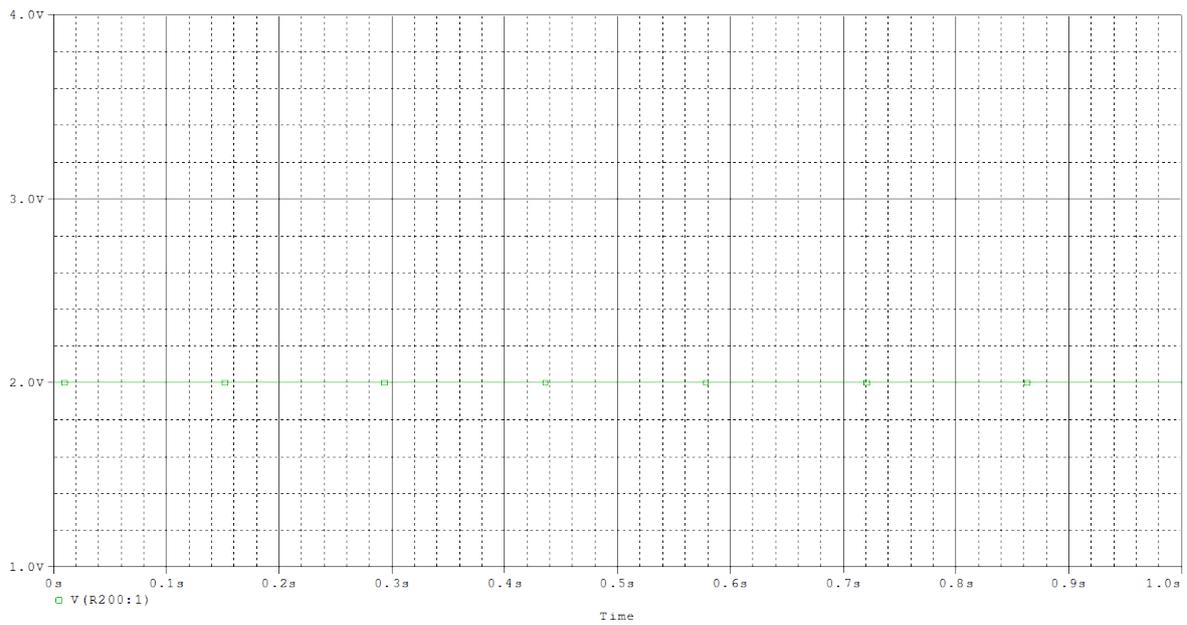
[Throttle 10% 개방, 출력 전압=0.516V, 정상치보다 0.016V(3.2%)초과]



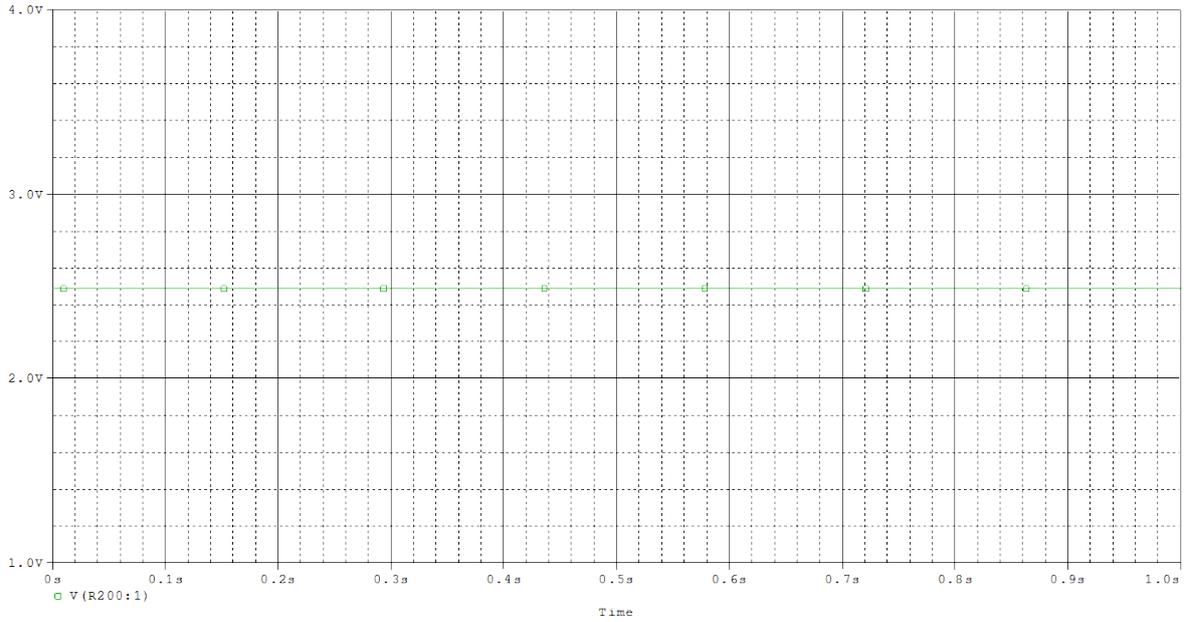
[Throttle 20% 개방, 출력 전압=1.02V, 정상치보다 0.012V(2%)초과]



[Throttle 30% 개방, 출력 전압=1.51V, 정상치보다 0.01V(0.67%)초과]



[Throttle 40% 개방, 출력 전압=2V, 정상치와 동일]



[Throttle 50% 개방, 출력 전압=2.49V, 정상치보다 0.01V(0.4%)미달]

4. 주파수 특성

