

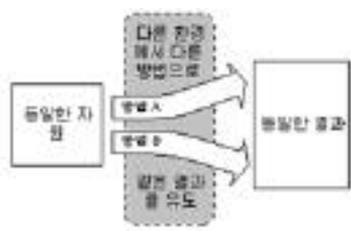
3 Embedded Systems RTOS Intel

(x86 Core)

Digital System Design Lab.
(promise@secsm.org)

RTOS(uC/OS-ii)가 Target

(Porting)



1.

8051 LED가 LED

Motorola 68K CPU
(HEX, BIN)

가. LED

가 ()

가

ASM(Assembler)

LED C, Port

ASM

ASM

.C

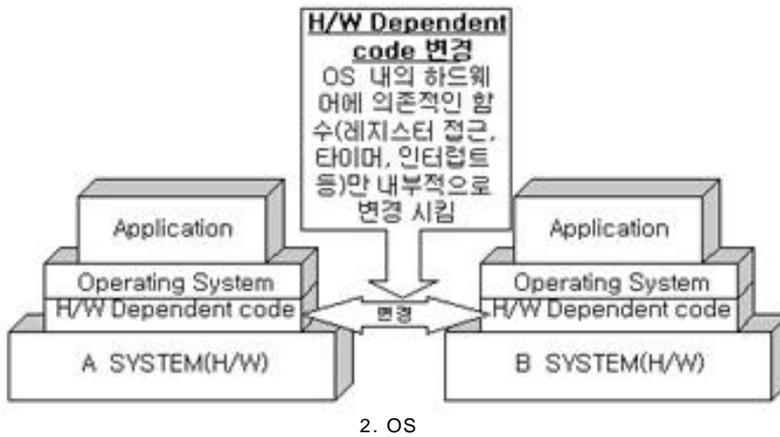
OS

가 C CPU

(, ,)

가

OS



OS

OS

JAVA

가

ARM7

PC x86

가

x86, 68K,

Target

OS

(,)

2. Host

Target

x86

PC

가

OS

PC

가

가

PC

가

Target

PC

Host

Target

가가

(,)

PC

Target

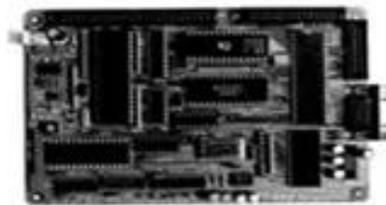
Host

PC x86

Target

PC

CPU



3.

Target

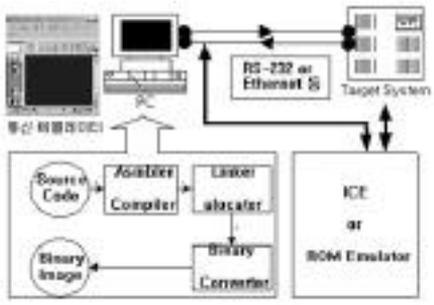
OS

Target

CPU,

Cross-Compile

PC



4.

Binary Utility

가

PC

가

232

PC

. Host(PC)

PC

가

가

Cross-Compile

Target

3. Target

RS-232

Target

(Linux)

(Unix)

GNU

Target

Cross-Compile

Host

Target

ROM

가

Binary Utility

. Binary Image

가 .exe

CPU가

. Target

Cross-Compile

가

Target

Binary Image

Bin-

util

. exe, bin, hex, elf

PC

Target

가

. Start-up, OS

Target

Serial Port

가

RS-



5. ROM Emulator

CPU . x86 MMX
 , 386DX/SX/SL 486DX/SX/DX2/
 SL/DX4 , 8086/
 8088 (186 core).
 CPU CPU



6. ICE Set

CPU 가
 가 ,
 386
 CPU Core가 386EX, AMD 486
 ELAN, NEC V Series

1. Intel Processor Series

	(MIPS)	(bits)	(bits)
8086	0.5	16	16
8088	0.5	16	8
80286	1.5	16	16
80386DX	10	32	32
80386SX	2.5	32	16
80386SL	5	32	32
80486DX	30	32	32
80486SX/SL	20	32	32

80486DX2 OverDrive	50	32	32
486DX4	70	32	32
Pentium 계열	125	32	64

Target
 ICE(In Circuit Emulator) PC
 Target
 가 Serial
 Ethernet 가
 가
 (Logic Analyzer)
 (Oscilloscope) 가 가
 Tar-
 get
 x86
 x86 x86 80286
 1982 80286 PC x86 CPU PC

. x86
 가 가 C Boland C 3.1
 Start-Up , BC3.1 C, C++ Turbo
 가 uC/OS
 가 C
 .exe PC-DOS
 x86 Target bin-util
 PC .가 , 가
 가 BC3.1
 (F9, Ctrl-F9) , PC-
 uC/OS . OS OS
 uC/
 OS Command
 가 Line x86
 , uC/OS
 가
 Single TASK OS
 uC/OS
 TASK가 ,
 . PC
 Target 68K ARM

1. x86



7. BC 3.1 IDE

2.
 BC3.1 (Integrated Development Environment)
 Option->Compiler->Code Generation... Advanced Code Generation...
 가 . Memory Model Floating Point Instruction Set
 CS(Code Segment), DS (Data Segment), SS(Stack Segment), ES(Extra Segment) 64Kbyte
 가 small, medium, compact, large 47가 가
 x86
 Large Floating Point

TASK가 OS Configuration (OS_CFG.H, INCLUDE.H). OS (Timer, Interrupt, Memory) , Inline ASM C (OS_CPU.H, OS_CPU_A.ASM, OS_CPU_C.C). Processor Dependent Code . uC/OS , Context Switch (ASM Context Switch).

4. Hardware Dependent Code 가 . OS_CPU.H : Critical Section . OS_CPU_A.ASM : _OSStartHighRdy, _OSCtxSw, _OSIntCtxSw, _OSTickISR Con- text Switching, Timer . OS_CPU_C.C : TASK가 OSTask- StkInit xxxHOOK

PC가 Tar- get x86 가

(1) OS_CPU.H

1. OS_CPU.H

```
typedef unsigned char BOOLEAN;
typedef unsigned char INT8U;
typedef signed char INT8S;
typedef unsigned int INT16U;
typedef signed int INT16S;
typedef unsigned long INT32U;
typedef signed long INT32S;
typedef float FP32;
typedef double FP64;
typedef unsigned int OS_STK;
#define BYTE INT8U
#define UBYTE INT8U
#define WORD INT16S
#define UWORD INT16U
#define LONG INT32S
#define ULONG INT32U
```

```
#define OS_CRITICAL_METHOD 2
#if OS_CRITICAL_METHOD == 1
#define OS_ENTER_CRITICAL() asm CLI
#define OS_EXIT_CRITICAL() asm STI
#endif
#if OS_CRITICAL_METHOD == 2
#define OS_ENTER_CRITICAL() asm (PUSHF; CLI)
#define OS_EXIT_CRITICAL() asm POPF
#endif
#define OS_STK_GROWTH 1
#define uCOS 0x20
#define OS_TASK_SW() asm INT uCOS
OS_CPU_EXT INT8U OSTickDOSCtr,
```

· Data Types :

· Critical Sections : Context Switching Critical Section

Inline ASM

ASM x86

· Stack Growth : x86

가 1

· Vector Number : Context Switching

· TimeTick : DOS Timer

(2) OS_CPU_A.ASM

Context Switching 가

x86

x86

CPU

가

9 8086

x86

3

14

TASK가

TASK

TASK

Context Switch

3. 80x86 Real-mode Register Map

15...8	7...0	NAME
AH	AL	General-Purpose Registers
BH	BL	
CH	CL	
DH	DL	
BP		Pointers
SP		
SI		Index Registers
DI		
IP		Instruction Pointer
SW		Status Word
CS		Segment Registers
SS		
DS		
ES		

· OSStartHighRdy() : OSStart()

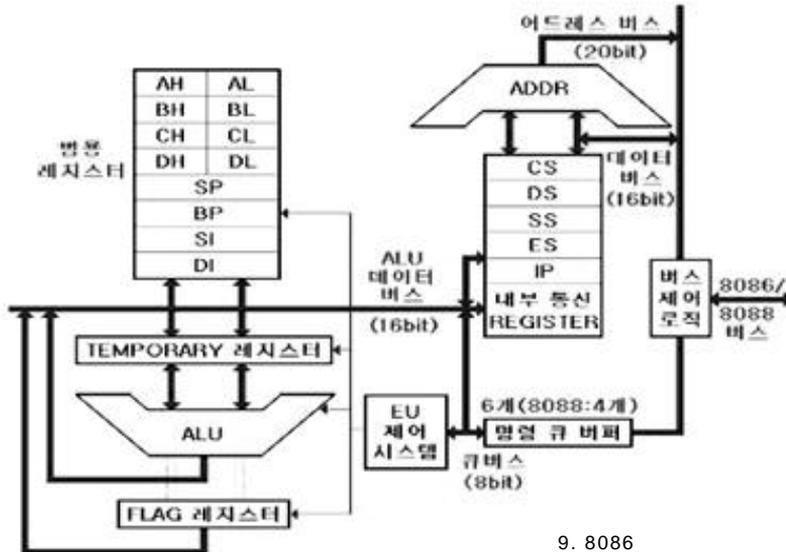
TASK

OSInit(), OSTaskCreate() OSTaskCreateExt()

가

(EU)

(BIU)



9. 8086

2. OSStartHighRdy

```

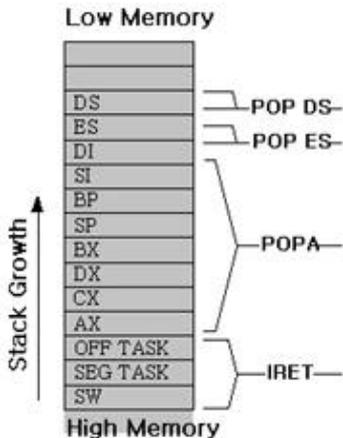
_OSStartHighRdy PROC FAR
MOV  AX, SEG _OSTCBHighRdy
MOV  DS, AX

CALL FAR PTR _OSTaskSwHook

INC  BYTE PTR DS: _OSRunning

LES  BX, DWORD PTR
DS: _OSTCBHighRdy
MOV  SS, ES:[BX+2]
MOV  SP, ES:[BX+0]
POP  DS
POP  ES
POPA
IRET
_OSStartHighRdy ENDP
    
```

OSTCBHighRdy
 TCB
 OSTaskSwHook
 OS_CPU_C.C
 가
 OSRunning 1 TASK
 TASK
 . POPA, PUSH



10. Stack Frame

10

TASK
 가 OSCtxSw() OSIntCtxSw
 ()
 · OSCtxSw() : TASK 가
 · OSIntCtxSw() : ISR(Interrupt Service
 Routine) OSIntExit() 가
 Task Context Switch
 TASK-Level Context Switch Interrupt-

3. OSCtxSw, OSIntCtxSw

_OSCtxSw PROC FAR	_OSIntCtxSw PROC FAR
PUSHA	ADD SP,10
PUSH ES	
PUSH DS	

```

MOV  AX, SEG _OSTCBCur
MOV  DS, AX

LES  BX, DWORD PTR DS: _OSTCBCur
MOV  ES:[BX+2], SS
MOV  ES:[BX+0], SP

CALL FAR PTR _OSTaskSwHook

MOV  AX, WORD PTR
DS: _OSTCBHighRdy+2
MOV  DX, WORD PTR
DS: _OSTCBHighRdy
MOV  WORD PTR DS: _OSTCBCur+2, AX
MOV  WORD PTR DS: _OSTCBCur, DX

MOV  AL, BYTE PTR DS: _OSPrioHighRdy
MOV  BYTE PTR DS: _OSPrioCur, AL

LES  BX, DWORD PTR
DS: _OSTCBHighRdy
MOV  SS, ES:[BX+2]
MOV  SP, ES:[BX]

POP  DS
POP  ES
POPA
IRET
_OSCtxSw ENDP | _OSIntCtxSw ENDP
    
```

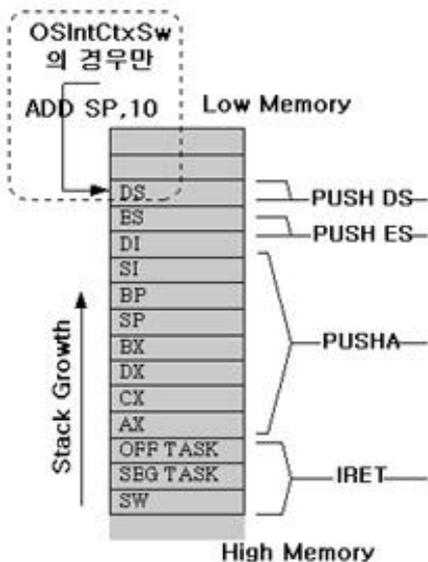
Level Context Switch

OSCtxSw DS, ES, TASK
 DI, SI, BP, SP, AX, BX, CX, DX
 TASK

OSIntCtxSw ISR

TASK Context

OSTickISR() : ticker interrupt
 가 Tick OS



11. OSCtxSw, OsintCtxSw

가 SS SP
 OS_TCB OSTCBHook
 OSTCBCur TCB(Task Control Block)
 TASK
 OSPrioCur(TASK)
 TCB SS SP TASK
 Context
 Switch가
 Context Switch
 가 TASK가 CPU
 가 TASK

4. PC Interrupt Vector Table

이전 DOS 사용 uC/OS의 DOS 사용

0x05	...	0x05	...
0x06		0x06	
0x07		0x07	
0x08	DOS Tick Handler(18.20669Hz)	0x08	OSTickBR0 (200Hz)
0x09	...	0x09	...
		0x80	OSCtxSw0
		0x81	DOS Tick Handler

TASK가 OSTickISR()가

4. OSTickISR

```

_OSTickISR PROC FAR
    PUSH A
    PUSH ES
    PUSH DS
    MOV AX, SEG _OSTickDOSctr
    MOV DS, AX
    INC BYTE PTR _OSIntNesting
    DEC BYTE PTR DS:_OSTickDOSctr
    CMP BYTE PTR DS:_OSTickDOSctr, 0
    JNE SHORT _OSTickISR1
    MOV BYTE PTR DS:_OSTickDOSctr, 11
    INT 081H
    JMP SHORT _OSTickISR2
_OSTickISR1 :
    
```

```

MOV AL, 20H
MOV DX, 20H
OUT DX, AL

_OSTickISR2:
CALL FAR PTR _OSTimeTick
CALL FAR PTR _OSIntExit
POP DS
POP ES
POPA
IRET

_OSTickISR BNDP
    
```

(3) OS_CPU_C.C

```

TASK
가 OSxxxHook
가 가
.
. OSTaskStkInit() : OSTaskCreate()
OSTaskCreateExt() TASK
. TASK가 TASK
(task), (pdata), TASK Top of Stack(ptos)
TASK (prio) OSTaskCreate()
OSTaskCreateExt()
    
```

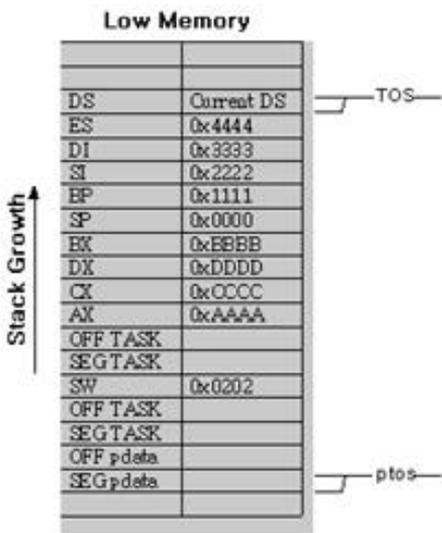
5. OSTaskStkInit

```

void +OSTaskStkInit (void (+task)(void
+pd), void +pdata, void +ptos, INT16U
opt)
{
    INT16U *stk;
    opt = opt;
    stk = (INT16U *)ptos;
    *stk-- = (INT16U)FP_SEG(pdata);
    *stk-- = (INT16U)FP_OFF(pdata);
    *stk-- = (INT16U)FP_SEG(task);
    *stk-- = (INT16U)FP_OFF(task);
    *stk-- = (INT16U)0x0202;
    *stk-- = (INT16U)FP_SEG(task);
    *stk-- = (INT16U)FP_OFF(task);
    *stk-- = (INT16U)0xAAAA;
    *stk-- = (INT16U)0xCCCC;
    *stk-- = (INT16U)0x0000;
    *stk-- = (INT16U)0x0000;
    *stk-- = (INT16U)0x1111;
    *stk-- = (INT16U)0x2222;
    *stk-- = (INT16U)0x3333;
    *stk-- = (INT16U)0x4444;
    *stk = _DS;
    return ((void *)stk);
}
    
```

Tick 가
 200Hz 11
 CPU
 Interrupt Nesting
 OSTimeTick Waiting, Pending TASK
 가 8059
 Tick 11
 x86
 , OSTickISR OSCtxSw
 (Timer)
 가 OSTickISR
 OS Tick 가
 PC

TASK



12.

```

#include "Include.h"
...
TASK
...
TASK
    
```

6-2. Maintest.c Main

```

void main (void)
{
    OSInit();
    PC_DOSSaveReturn();
    PC_VectSet(uCOS, OSCtxSw);
    RandomSem = OSSEMCreate(1);
    OSTaskCreate(TaskStart, (void *)0, (void *)
    &TaskStartStk[TASK_STK_SIZE - 1], 0);
    OSStart();
}
    
```

TASK

OS

1.

TASK

TASK

TASK

OSInit()

PC_DOSSaveReturn()

PC_xxx()

OS

x86 PC

"Maintest.c"

Timer

PC.C,

PC.H

LCD Display

TASK

OSCtxSw()

OSTaskCreate()

TASK

Osstart()

uC/OS

가 x86

TASK

TASK

0 가

TaskStart

6-1. Maintest.c

```

#include "includes.h"
#define TASK_STK_SIZE 512
#define N_TASKS 10
    
```

```

OS_STK
TaskStk[N_TASKS][TASK_STK_SIZE];
OS_STK TaskStartStk[TASK_STK_SIZE];
char TaskData[N_TASKS];
OS_EVENT *RandomSem;

void Task(void *data);
void TaskStart(void *data);
    
```

() OS Tick
 Main()
 TASK TASK
 Tick TASK

6-4. Maintest.c Task

```
void Task(void *data)
{
    UBYTE x;
    UBYTE y;
    UBYTE err;

    for(;;){
        OSSemPend(RandomSem, 0, &err);
        x = random(30);
        y = random(16);
        OSSemPost(RandomSem);

        PC_DispChar(x, y + 5, *(char *)data,
        DISP_FGND_LIGHT_GRAY);
        OSTimeDly(1);
    }
}
```

6-3. Maintest.c TaskStart

```
void TaskStart(void *data)
{
    UBYTE i;
    WORD key;

    OS_ENTER_CRITICAL();
    PC_VectSet(0x08, OSTickISR);
    PC_SetTickRate(OS_TICKS_PER_SEC);
    OS_EXIT_CRITICAL();

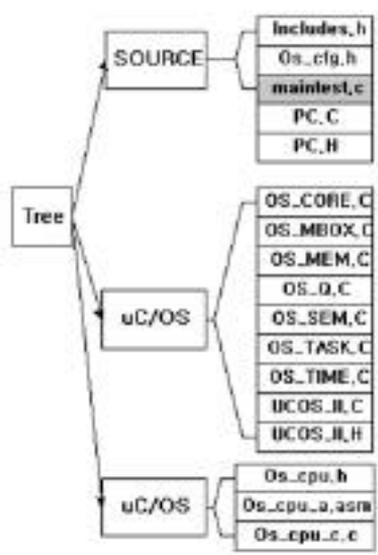
    OSTaskCreate(Task, (void *)&TaskData[i], (void *)
    &TaskStk[i][TASK_STK_SIZE - 1], i + 1);

    for(;;){
        if(PC_GetKey(&key) == TRUE){
            if(key == 0x1B){
                PC_DOSReturn();
            }
        }
    }
}
```

56
 가
 TASK

```
OSTimeDlyHMSM(0, 0, 1, 0);
}
}
```

2.



TASK Task
 0
 가 Task() 가
 가 TASK



```

OS_CFG.H          TASK
IPC              TASK

UCOS_II.C, .H    OS_XXX.C

                TASK

                가

.PC

BC3.1           Project -> Open Project...
                Project -> Add Item...

                MAINTEST.C
OS_CPU_C.C, OS_CPU_A.ASM, PC.C, UCOS_II.C
C              가
                , Library, Include

```

```

.AUTODEPEND

.PATH.obj = OBJ

CC = bcc *MAINTEST.CPG
TASM = TASM
TLIB = tlib
TLINK = tlink
LIBPATH = C:\Wbc31\LIB
INCLUDEPATH = C:\Wbc31\INCLUDE

.c.obj:
$(CC) -c {$<}

.cpp.obj:
$(CC) -c {$<}

EXE_dependencies = W
os_cpu_a.obj W
os_cpu_c.obj W
MAINTEST.obj W
pc.obj W
ucos_ii.obj

```

```

obj\WMAINTEST.exe:
MAINTEST.cfg $(EXE_dependencies)
$(TLINK) /v/s/c/p-/L$(LIBPATH)
@&&|
c0l.obj+
obj\Wos_cpu_a.obj+
obj\Wos_cpu_c.obj+
obj\WMAINTEST.obj+
obj\Wpc.obj+
obj\Wucos_ii.obj
obj\WMAINTEST.obj\WMAINTEST
emu.lib+
math.lib+
cl.lib
|
*
os_cpu_a.obj:
MAINTEST.cfg ..\Wx861\Wos_cpu_a.asm
$(TASM) /MX /ZI
/O ..\WX86L\WOS_CPU_A.ASM,OBJ\WOS
_CPU_A.OBJ

os_cpu_c.obj:
MAINTEST.cfg ..\Wx861\Wos_cpu_c.c
$(CC) -c ..\W

MAINTEST.obj: MAINTEST.cfg
MAINTEST.c

pc.obj: MAINTEST.cfg ..\Wsource\Wpc.c
$(CC) -c ..\Wsource\Wpc.c

ucos_ii.obj:
MAINTEST.cfg ..\Wsource\Wucos_ii.c
$(CC) -c ..\Wsource\Wucos_ii.c

MAINTEST.cfg: makefile.mak
copy &&|
-m1 -1 -v -G -O -Og -Oe -Om -Ov -Ol -
Ob -Op -Oi -Z -k -vi- -wpro -weas -
wpre -nOBJ
-I$(INCLUDEPATH)
-L$(LIBPATH)
-P-.C

```

```

        Include.h, UCOS_II.C
        F9
Maintest.exe
        Command Line
        .bat      Makefile
        Makefile
        . Makefile
Boland C
        CPU
        (   Makefile  bat
          ).
        PC Target      uC/OS
        OS
        PC
        uC/OS 68K, ARM7
        가      386EX      x86
        가
        68K Core Communication Pro-
cessor가      MC68302 uC/
OS

```



```

--      --
        가
        memory, interrupt, timer
        Kernel  stack
        C      code(
        ,      stack, heap) data(
        )
        .      . stack
        Kernel  Task
        Interrupt  Signal
        . Timer      Timer
        가
        Time
        가      OS
가 가
uC/OS-II      가
가      가
C      가 ,
        ,      Timer
        ,
        CPU      Instruc-
tion      . Data Types

```



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