

그림 1 sinusoidal

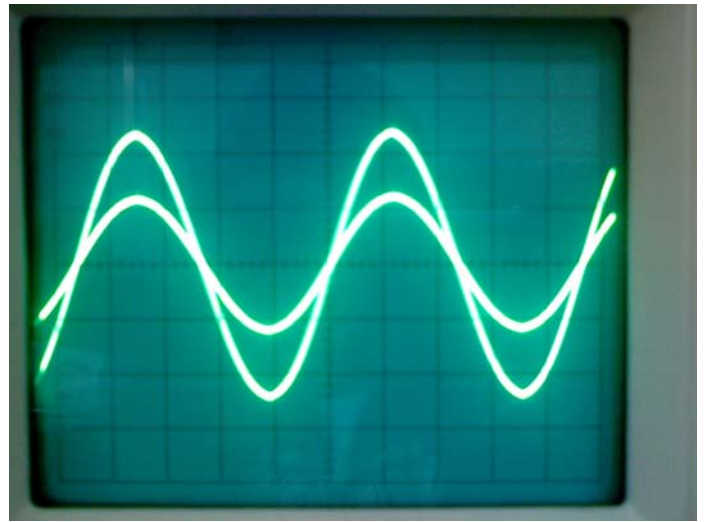


그림 4

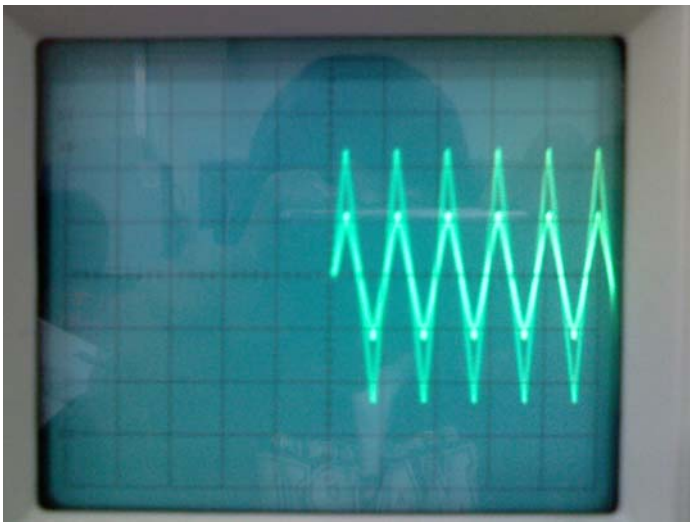


그림 2 triangular

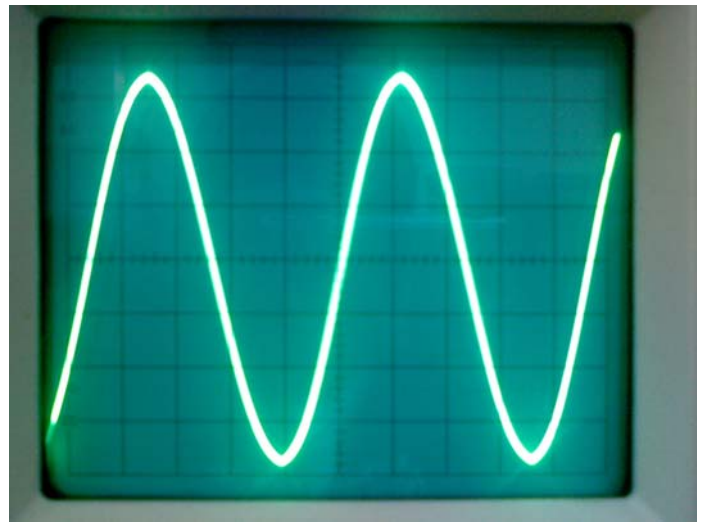


그림 5 v-mode add

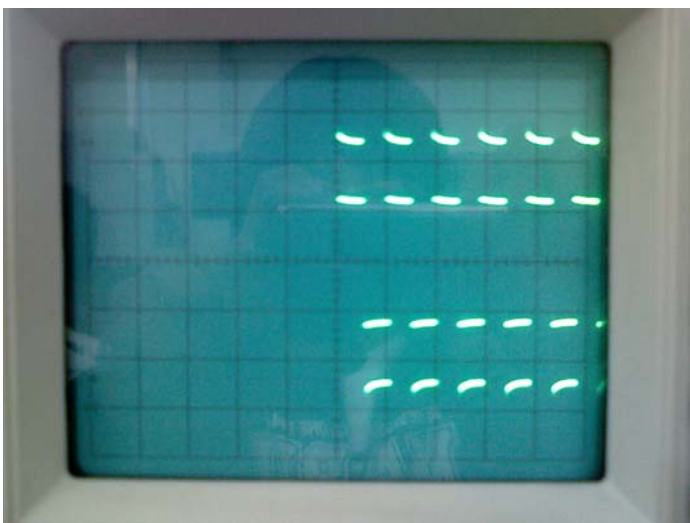


그림 3 square

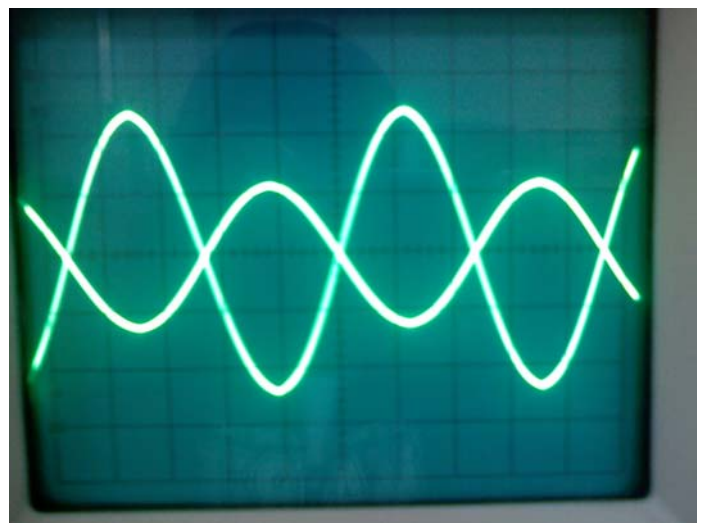


그림 6 v-mode dual, CH2 inversion

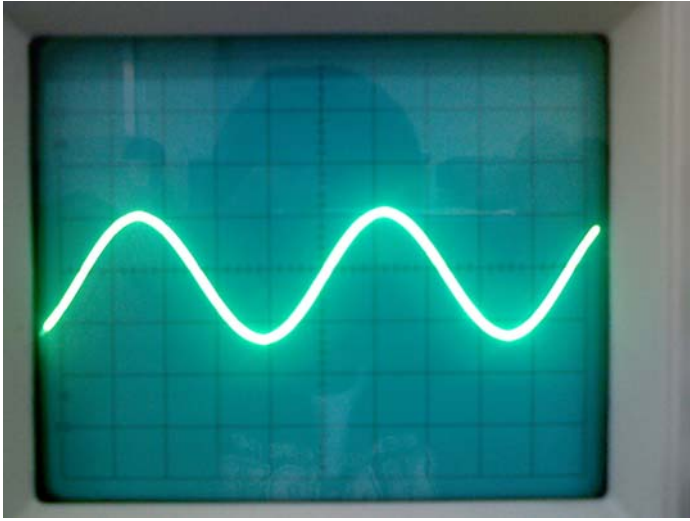


그림 7 v-mode add, CH2 inversion

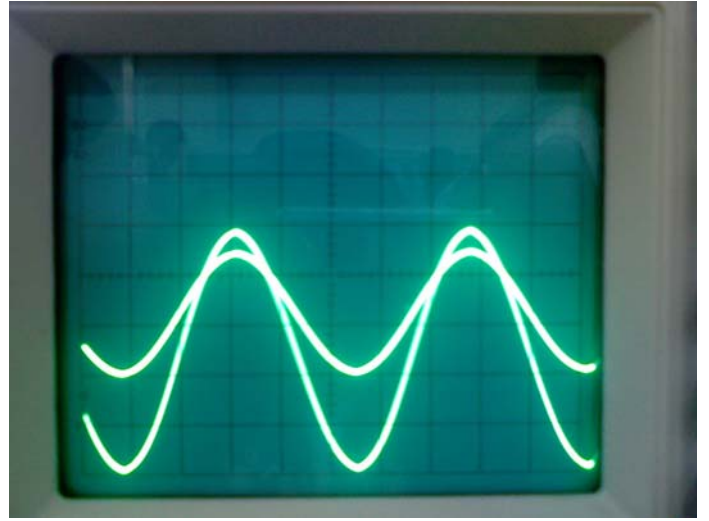


그림 10 DC OFFSET 단자 조절

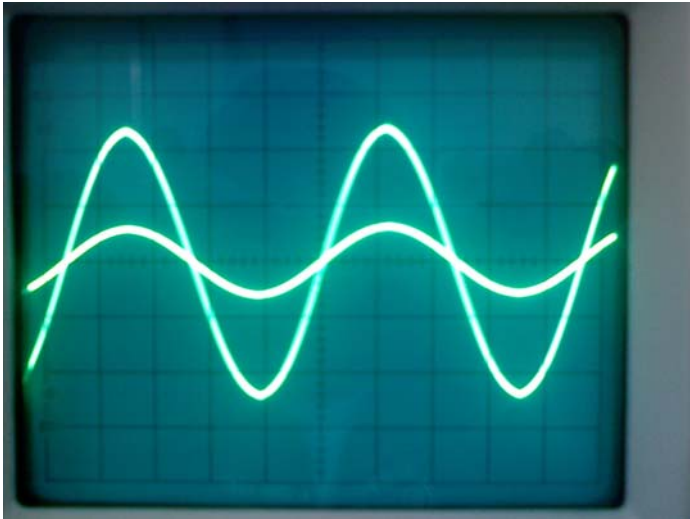


그림 8 v-mode dual, CH2 volts/div 세팅값 2배

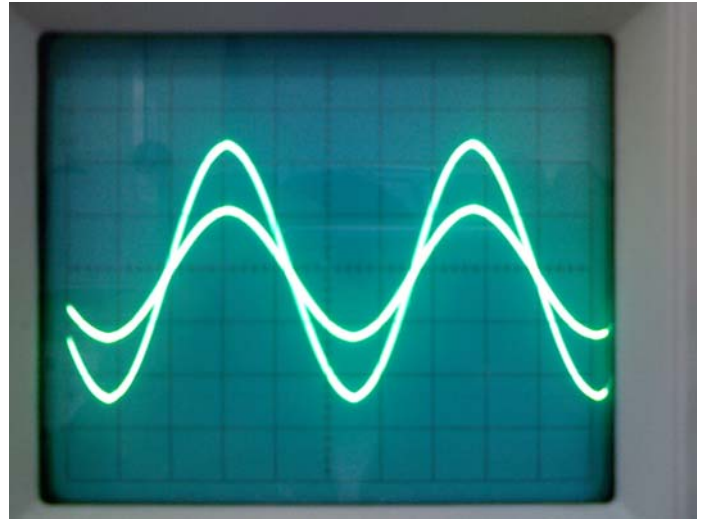


그림 11 DC OFFSET 단자 deactivation

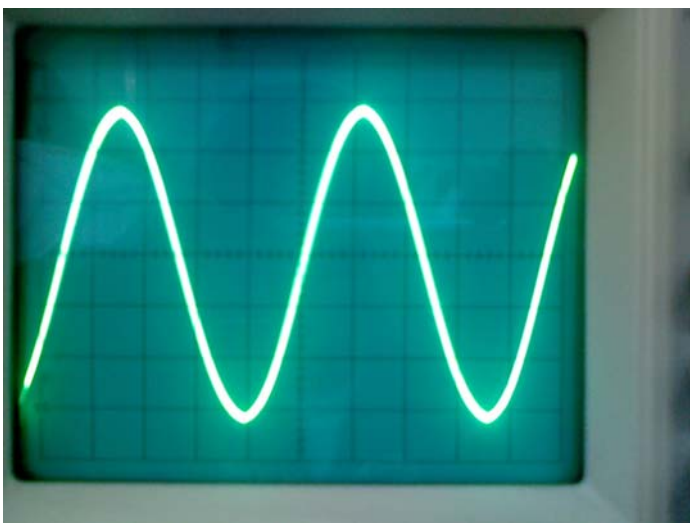


그림 9 v-mode add, CH2 volts/div 세팅값 2배