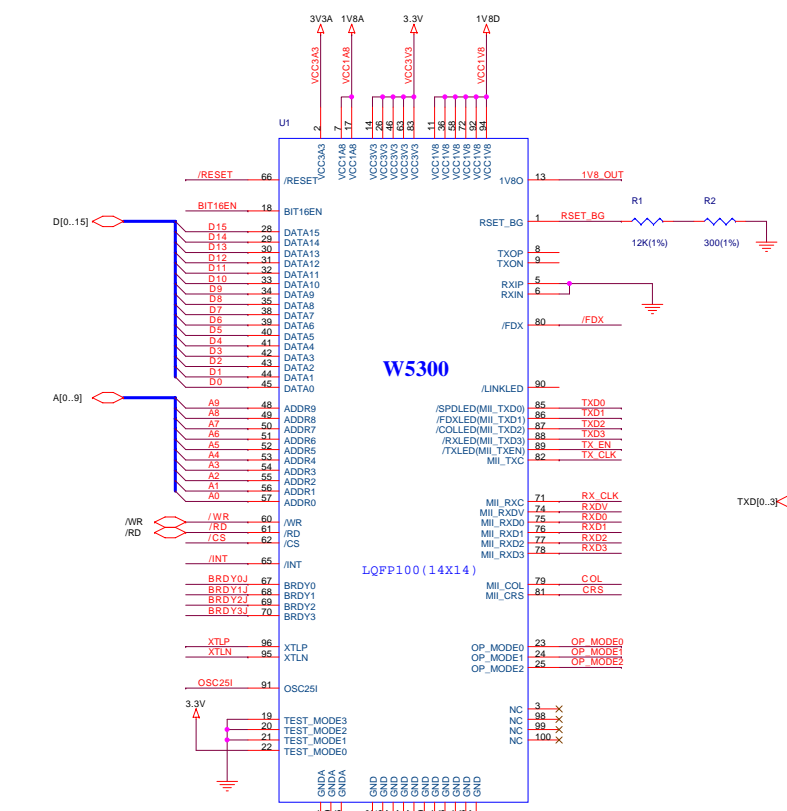
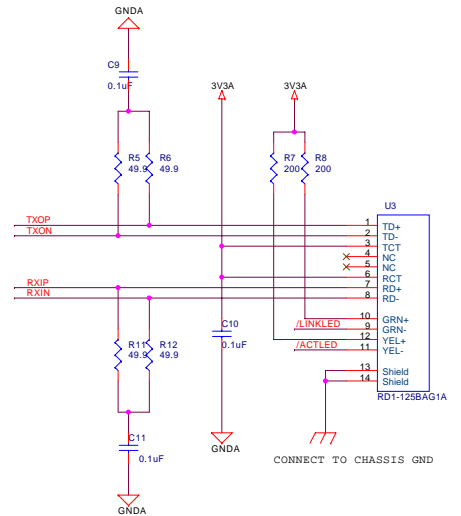
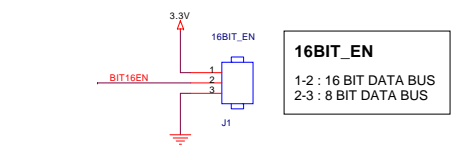
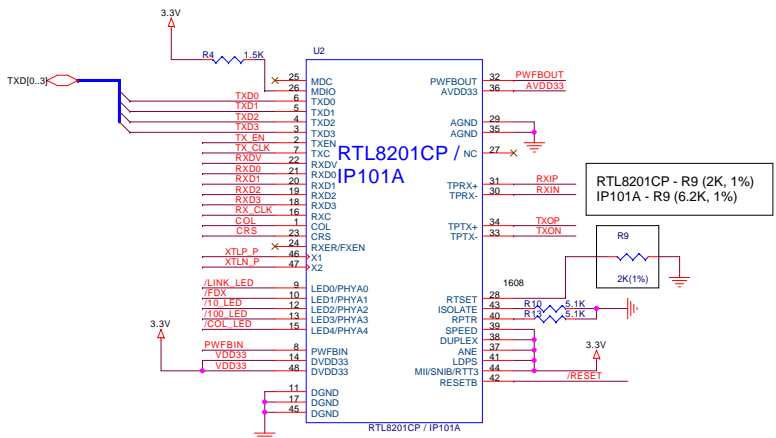
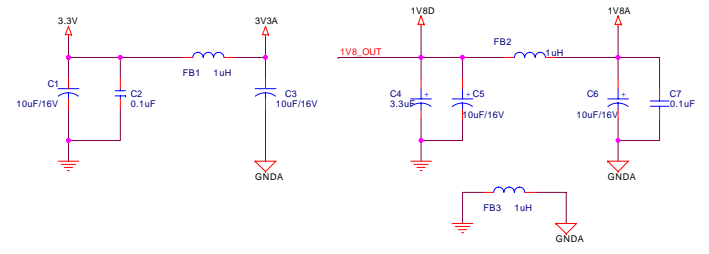


The analog and digital ground planes should be as large and intact as possible. If the ground plane is large enough, the analog and digital grounds can be separated, which is the ideal configuration. However, if the total ground plane is not sufficiently large, partition of the ground plane is not a good idea. In this case, all the ground pins can be connected together to a larger single and intact ground plane.(remove FBS, and change 'GNDA' to 'GND'.)



This schematic sets TEST\_MODE to 0001b. You could set TEST\_MODE 0001b or 0010b in external PHY mode. Refer to datasheet's detailed description.

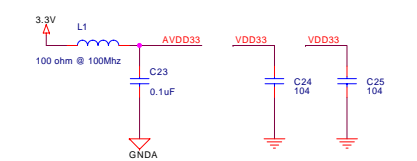
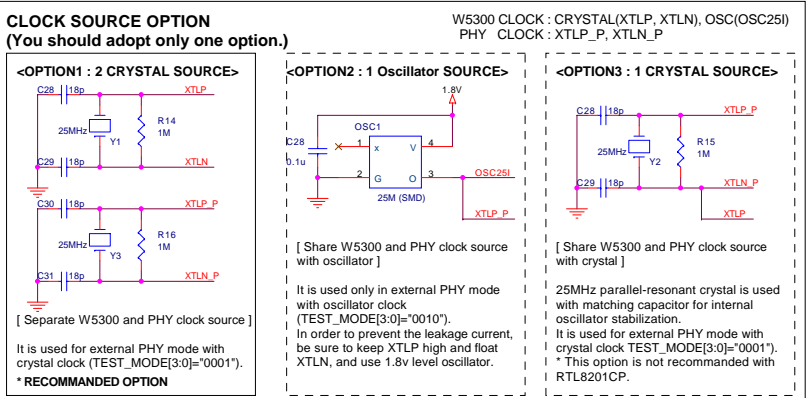
CLOCK SOURCE OPTION 1 : 0001b  
 CLOCK SOURCE OPTION 2 : 0010b  
 CLOCK SOURCE OPTION 3 : 0001b



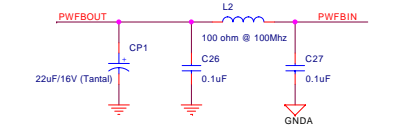
< Transformer Specification >

TURN RATIO : TX&RX = 1CT:1CT  
 INDUCTANCE : 350uH MIN.

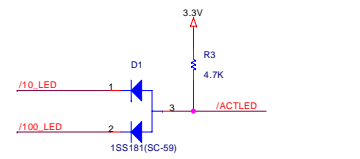
\* In case of using External PHY mode, use the transformer which is suitable for external PHY specification.



Place L1, C23, C24, C25 as close to each power pin as possible.



Place CP1, C26, L2 close to PWFOUT and place C27 close to PWFBIN.



Title		
W5300 External PHY mode reference schematic		
Size	Document Number	Rev
C	<Doc>	1.0
Date:	Thursday, March 20, 2008	Sheet 1 of 1