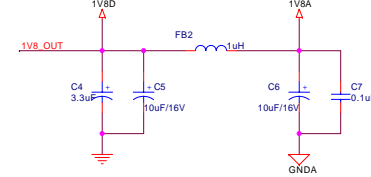
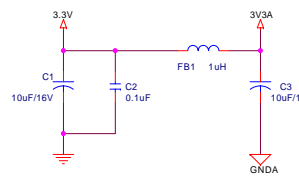
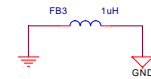
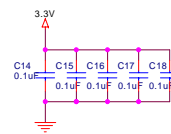
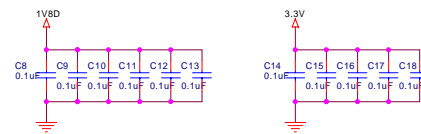
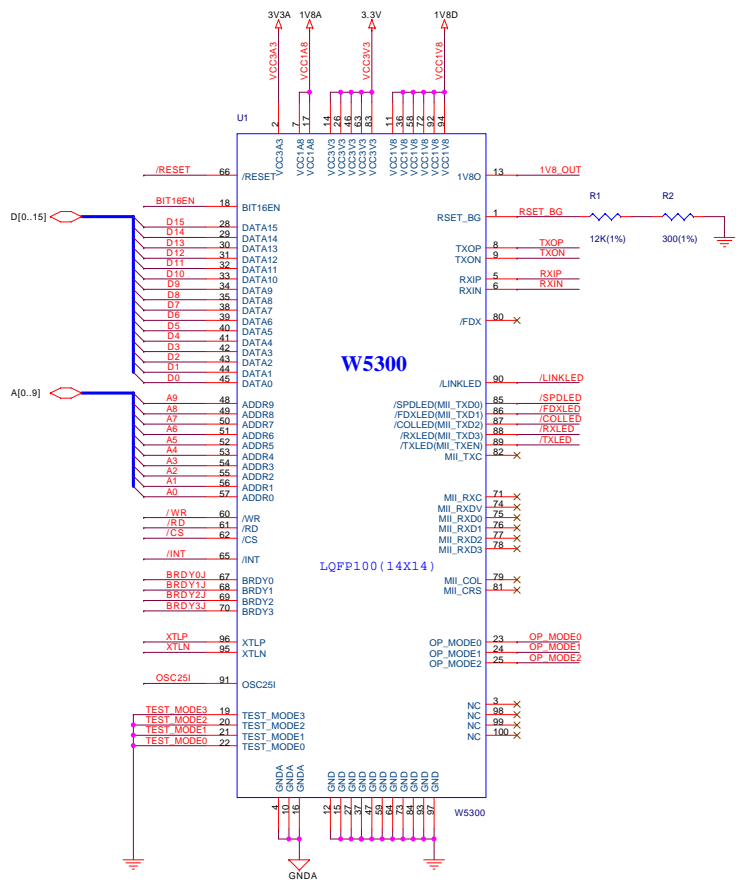
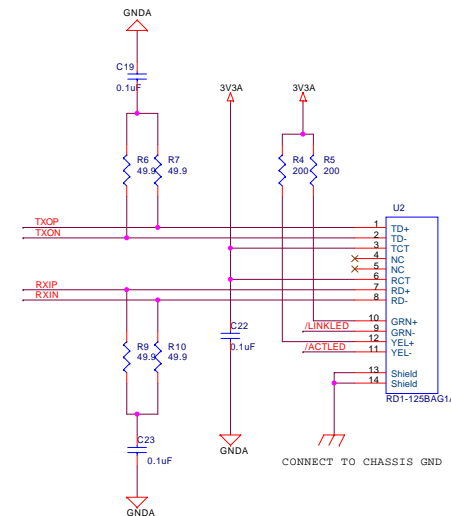
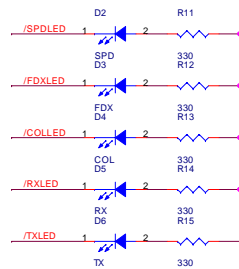
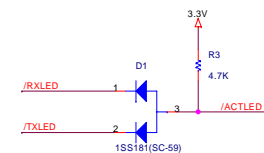


The analog and digital ground planes should be as large and intact as possible. If the ground plane is large enough, the analog and digital grounds can be separated, which is the ideal configuration. However, if the total ground plane is not sufficiently large, partition of the ground plane is not a good idea. In this case, all the ground pins can be connected together to a larger single and intact ground plane.(remove FB3, and change 'GNDA' to 'GND'.)



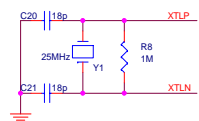
Place FB1, C1, C2, C3 as close to each power pin as possible.

Place C4, C5, FB2 close to 1V8_OUT and place C6, C7 close to 1V8A pin.

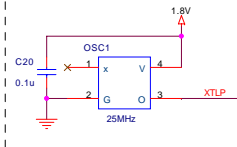


CLOCK SOURCE OPTION
(You should adopt only one option.)

<OPTION1 : 2 CRYSTAL SOURCE>

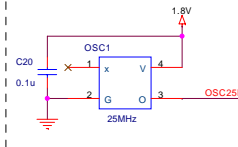


<OPTION2 : Oscillator SOURCE 1>

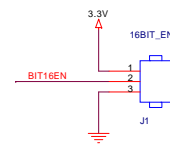


When using oscillator, be sure to use 1.8v level oscillator and connect only to XTLP, and let be float XTLN.

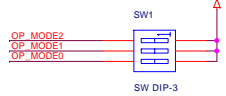
<OPTION3 : Oscillator SOURCE 2>



In order to prevent the leakage current, be sure to keep XTLP high and float XTLN, and use 1.8v level oscillator.



16BIT_EN
1-2 : 16 BIT DATA BUS
2-3 : 8 BIT DATA BUS



SW1
000 : Auto-negotiation enable with all capabilities
001 : Auto-negotiation with 100 BASE-TX FDX/HDX ability
010 : Auto-negotiation with 10 BASE-T FDX/HDX ability
011 : Reserved
100 : Manual selection of 100 BASE-TX FDX
101 : Manual selection of 100 BASE-TX HDX
110 : Manual selection of 10 BASE-T FDX
111 : Manual selection of 10 BASE-T HDX

< Transformer Specification >
TURN RATIO : TX&RX = 1CT:1CT
INDUCTANCE : 350uH MIN.